



DATA BUS TRANSIENT SUPPRESSOR

Case Material: Molded Plastic. UL Flammability Classification

Moisture Sensitivity: Level 1 per J-STD-020D

Orientation: See Diagram Below

Marking Information: See Page 3

Ordering Information: See Page 3

Weight: 0.006 grams (approximate)

Terminals: Finish — Matte Tin annealed over Alloy 42

leadframe. Solderable per MIL-STD-202, Method 208

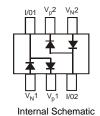
Features

- ESD Protection >30kV (Human Body Model) (Note 1)
- Ultra-Small Surface Mount Package
- Protects 2 Data Lines
- Low Leakage <25nA
- Low Capacitance 3pF Typ.
- Protects USB 2.0 and USB 1.1
- Lead, Halogen and Antimony Free, RoHS Compliant "Green" Device (Notes 4, 5 and 6)

IEC Compatibility (Note 1)

- 61000-4-2 (ESD) Air-30kV Contact-30kV
- 61000-4-4 (EFT) 40A, 5/50 ns
- 61000-4-5 (Surge) 8x20μs, 20 Amperes

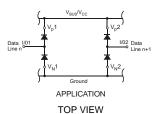




Mechanical Data

Case: SOT-363

Rating 94V-0



Maximum Ratings @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit	
Non-Repetitive Peak Reverse Voltage		V_{RM}	100	V
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage		Vrrm Vrwm Vr	80	V
Forward Continuous Current (Note 2)		I _{FM}	500	mA
Repetitive Peak Forward Current @ $T_p = 5\mu s$, $f = 50kHz$ (I	Note 2)	I _{FRM}	1000	mA
Non-Repetitive Peak Forward Surge Current	@ t = 1.0μs @ t = 1.0s	I _{FSM}	20 2.0	А
Clamping Voltage @ I _{pp} = 20A (Note 3) 8x20µs Waveform		Vc	16	V

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 2)	P_{D}	200	mW
Thermal Resistance, Junction to Ambient Air (Note 2)	$R_{ heta JA}$	625	°C/W
Operating and Storage Temperature Range	T_J , T_{STG}	-65 to +150	°C

Notes:

1. Tested with V_{P} connected to V_{N} to simulate appropriate $V_{\text{BUS}}/V_{\text{CC}}$ decoupling to ground.

TOP VIEW

- 2. Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch; pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.
- 3. Referenced to V_P or V_N .
- No purposefully added lead. Halogen and Antimony Free.
- Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php.
- Product manufactured with Data Code V9 (week 33, 2008) and newer are built with Green Molding Compound. Product manufactured prior to Date Code V9 are built with Non-Green Molding Compound and may contain Halogens or Sb₂O₃ Fire Retardants.

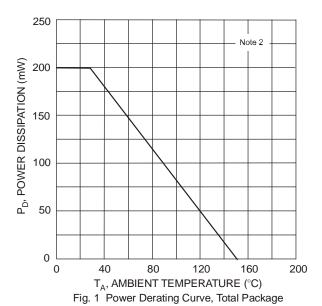
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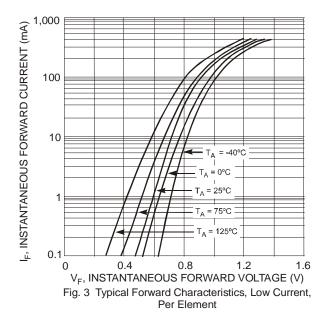


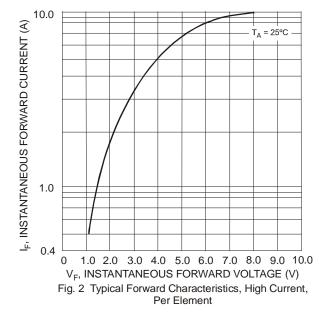
Electrical Characteristics @TA = 25°C unless otherwise specified

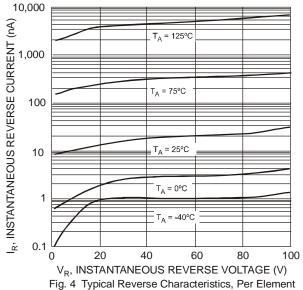
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 7)	$V_{(BR)R}$	80		_	V	$I_R = 100 \mu A$
		0.62		0.72		$I_F = 5.0 \text{mA}$
Forward Voltage	V _F	_	_	0.93	. v	$I_F = 20 \text{mA}$
i olward voltage		_		1.0		$I_F = 100 \text{mA}$
		_		1.25		$I_F = 150 \text{mA}$
	I _R	lr —		100	nA	$V_R = 70V$
Reverse Current (Note 7)			_	50	μΑ	$V_R = 75V, T_J = 150^{\circ}C$
Neverse Guiterit (Note 1)				30	μΑ	$V_R = 25V, T_J = 150^{\circ}C$
				25	nA	$V_R = 20V$
Capacitance, Between I/O Lines (I/O1 & I/O2)	C_{LL}		2.5	4.0	рF	$V_R = 0V$, $f = 1.0MHz$
Capacitance Between I/O Line and Ground	C_{LG}		3.3	5.3	рF	$V_R = 0V, f = 1.0MHz$
Reverse Recovery Time	t _{rr}	_	_	4.0	ns	$V_R = 6V$, $I_F = 5mA$

Notes: 7. Short duration pulse test used to minimize self-heating effect.

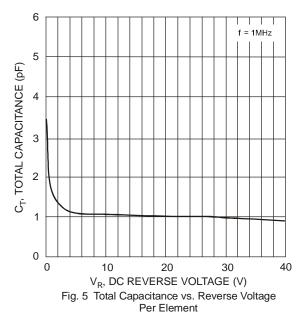












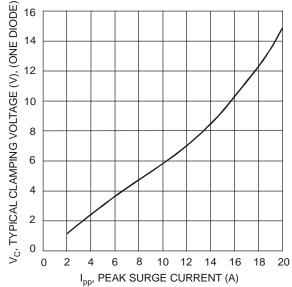
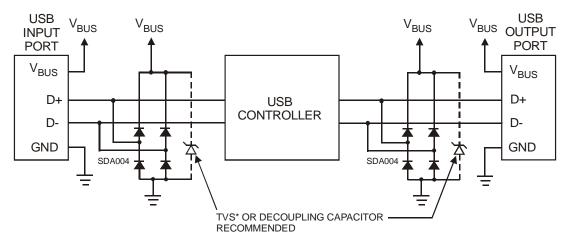


Fig. 6 6100-4-5 8x20 μs Lightning Surge Response, Per Element



* MMBZ6V8AL OR EQUIVALENT

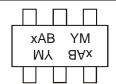
ESD PROTECTION - USB APPLICATION

Ordering Information (Note 8)

Part Number	Case	Packaging
SDA004-7	SOT-363	3000/Tape & Reel

Notes: 8. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

Marking Information



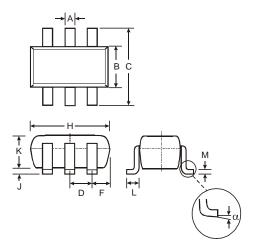
KAB or JAB = Product Type Marking Code YM = Date Code Marking Y = Year ex: R = 2004 M = Month ex: 9 = September

Date Code Key

Year	2004	20	05	2006	2007	20	800	2009	2010	21	11	2012
Code	R	9	S	T	U	,	V	W	Х	,	Y	Z
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

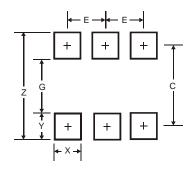


Package Outline Dimensions



	SOT-363					
Dim	Min	Max				
Α	0.10	0.30				
В	1.15	1.35				
С	2.00	2.20				
D	0.65 Nominal					
F	0.30	0.40				
Н	1.80	2.20				
J	_	0.10				
K	0.90	1.00				
L	0.25	0.40				
M	0.10	0.25				
α	0°	8°				
All Dimensions in mm						

Suggested Pad Layout



Dimensions	Value (in mm)
Z	2.5
G	1.3
Х	0.42
Υ	0.6
С	1.9
E	0.65

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